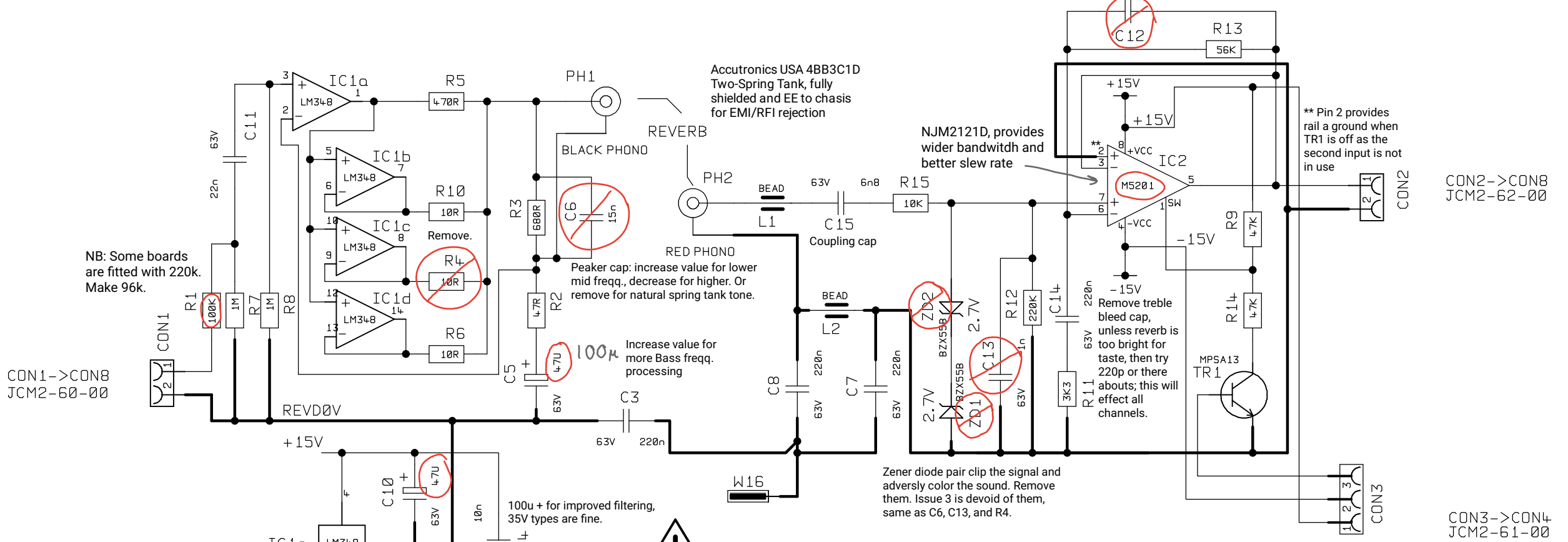


DWG.No. TL10-63-02.DGM

NB: Some boards are fitted with 220k. Make 96k.

Smoothing cap removes harshness from clipping zener diodes, otherwise this is the NFB peaker. Remove for optimal spring fidelity. 4.70p 50V



!

COUNTRY IDENT:- B, C, J, M, T, U
TYPE 19198
T1AMP

COUNTRY IDENT:- A, D, E, H, K, S, V, X
TYPE 19195
T1AMP

!

STOCK No. TXMA-00066
PART No. D2179

STOCK No. TXMA-00062
PART No. D2135

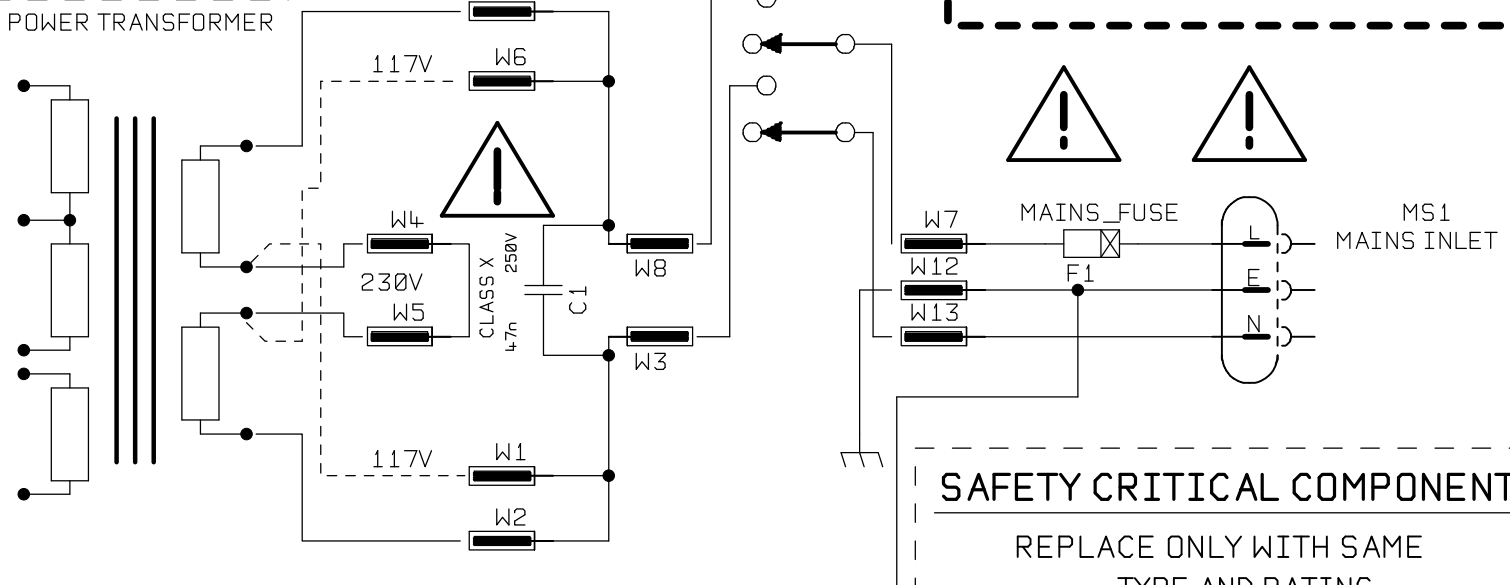
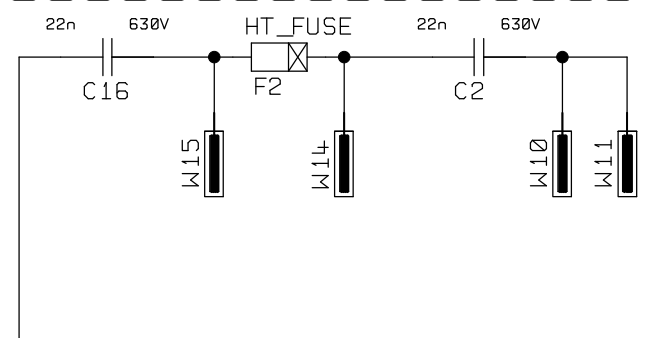
STOCK No. TXMA-00061
PART No. D2105

POWER TRANSFORMER

!

COUNTRY IDENT:- B, C, J, M, T, U
F1=T4AMP TYPE 19343
MAINS SWITCH=STOCK No. S151
PART No. 1310NB5NKR125V

COUNTRY IDENT:- A, D, E, H, K, S, V, X
F1=T2AMP TYPE 19195
MAINS SWITCH=STOCK No. S217
PART No. 1610NRNB5NKR250V



!

SAFETY CRITICAL COMPONENT

REPLACE ONLY WITH SAME TYPE AND RATING.

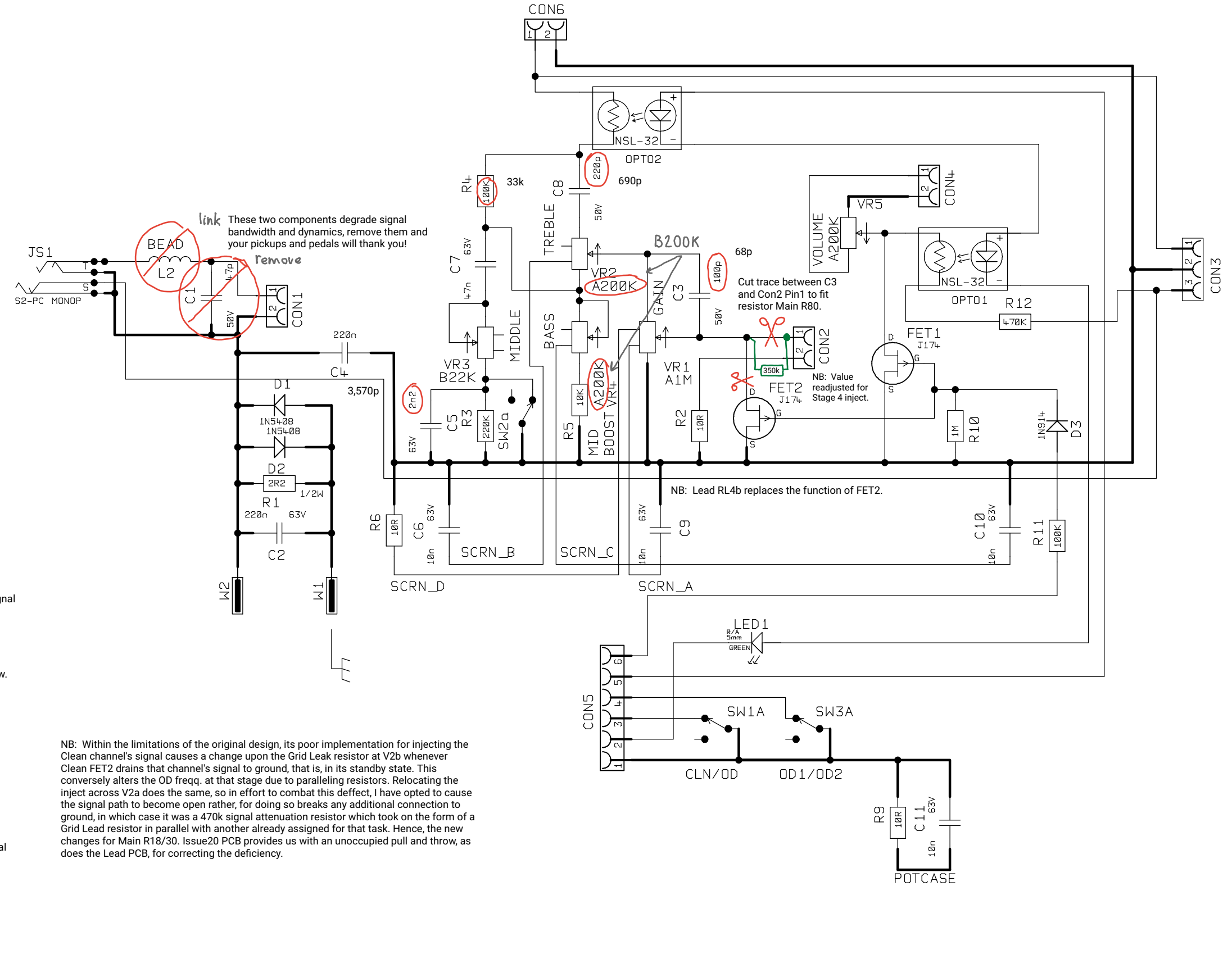
The Vesp̄erādo m̄od.

4	1663	21/9/98	ALL OTHER HOLES = X = (THROUGH PLATED)																MATERIAL	DIMENSIONS IN		<p>© MARSHALL AMPLIFICATION PLC DENBIGH ROAD, BLETCHLEY, MILTON KEYNES, MK1 10Q. TEL (01908) 375411 FAX (01908) 376118</p>	TITLE	JCM2000 TSL100/TSL122 REVERB/MAINS INPUT			
3	1524	17/11/97	A																		MATERIAL THICKNESS		TOLERANCE (UNLESS OTHERWISE STATED)		ISS	4	
2	1466	1/8/97	B																		DRAWN		SG	DATE	28-12-96		
1	PRODUCTION ISSUE	30/4/97	C																		CHECKED			DATE		MODEL	TSL100/TSL122
ISS	ECO NUMBER	DATE	D																		APPROVED			DATE			

ignorelayers 7

DWG.No. TL10-65-02.DGM

CLEAN CHANNEL



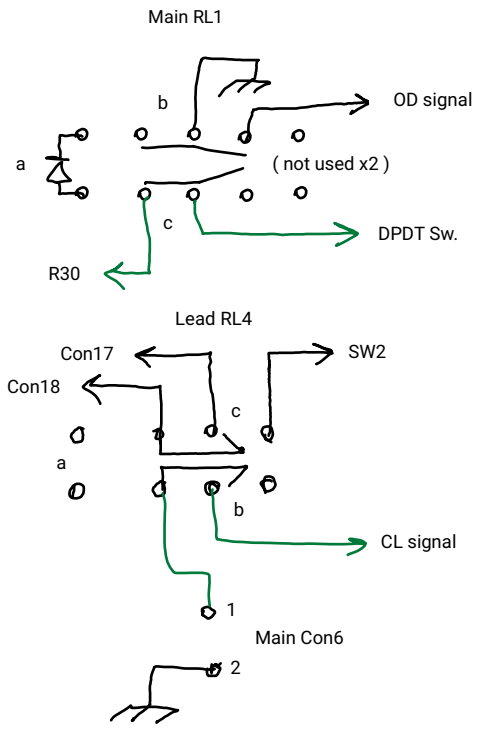
link These two components degrade signal bandwidth and dynamics, remove them and your pickups and pedals will thank you!

Cut trace between C3 and Con2 Pin1 to fit resistor Main R80.

NB: Value readjusted for Stage 4 inject.

NB: Lead RL4b replaces the function of FET2.

NB: Within the limitations of the original design, its poor implementation for injecting the Clean channel's signal causes a change upon the Grid Leak resistor at V2b whenever Clean FET2 drains that channel's signal to ground, that is, in its standby state. This conversely alters the OD freqq. at that stage due to paralleling resistors. Relocating the inject across V2a does the same, so in effort to combat this defect, I have opted to cause the signal path to become open rather, for doing so breaks any additional connection to ground, in which case it was a 470k signal attenuation resistor which took on the form of a Grid Lead resistor in parallel with another already assigned for that task. Hence, the new changes for Main R18/30. Issue20 PCB provides us with an unoccupied pull and throw, as does the Lead PCB, for correcting the deficiency.



The Vesperado mod.

4	PRE-PROD MODS (1567)	23/3/98	ALL OTHER HOLES =				X = (THROUGH PLATED)			
3	1567	15/1/98	A	E	J	N				
2	PRE-PRODUCTION	2/12/97	B	F	K	P				
1	PRE-PRODUCTION	20/10/97	C	G	L	Q				
ISS	ECO NUMBER	DATE	D	H	M	R				

MATERIAL		DIMENSIONS IN	
MATERIAL THICKNESS		TOLERANCE (UNLESS OTHERWISE STATED)	
DRAWN SG	DATE 1-5-97	MODEL TSL100/TSL122	
CHECKED	DATE		
APPROVED	DATE		



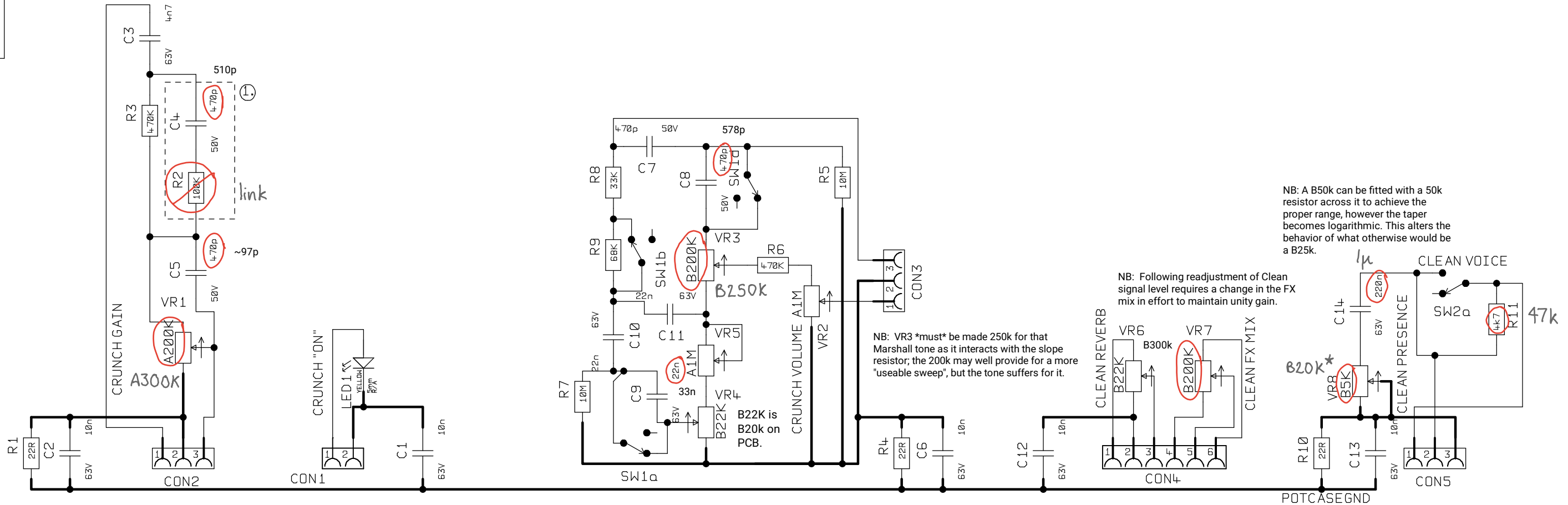
TITLE	
CLEAN CHANNEL	
DWG.No.	ISS
TL10-65-02.DGM	4

ignore layers 7

DWG.No. TL10-66-01.DGM

CRUNCH CHANNEL

1. C10 & R12 NOT FITTED



NB: A B50k can be fitted with a 50k resistor across it to achieve the proper range, however the taper becomes logarithmic. This alters the behavior of what otherwise would be a B25k.

NB: Following readjustment of Clean signal level requires a change in the FX mix in effort to maintain unity gain.

NB: VR3 *must* be made 250k for that Marshall tone as it interacts with the slope resistor; the 200k may well provide for a more "useable sweep", but the tone suffers for it.

B20k*

The Vesperado mod.

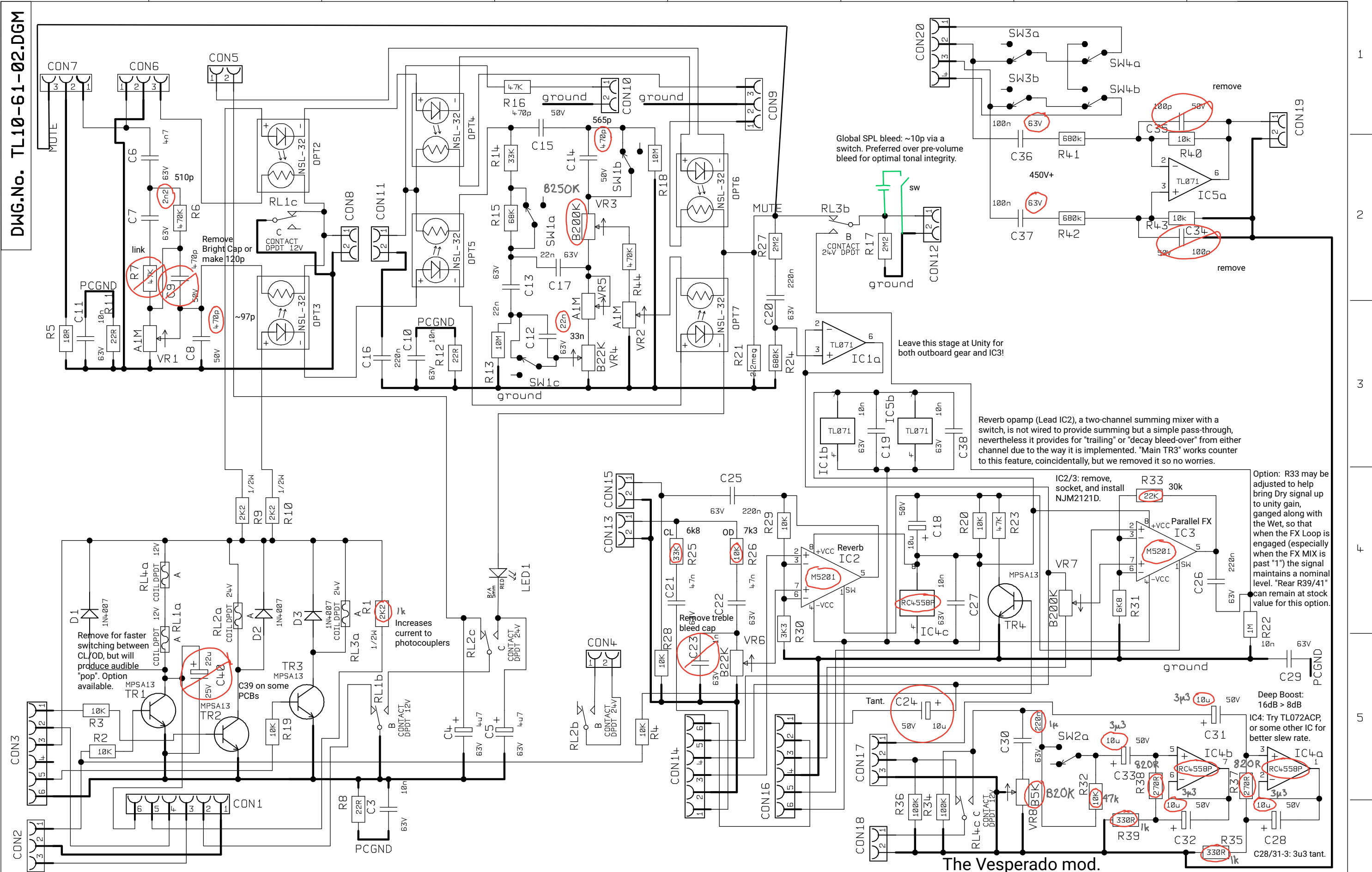
		ALL OTHER HOLES =		X = (THROUGH PLATED)	
2	PRE-PROD MODS (1567)	23/3/98	A	E	J
1	PRE-PRODUCTION	20/10/97	B	F	K
ISS	ECO NUMBER	DATE	C	G	L
			D	H	M
					N
					P
					Q
					R

MATERIAL	DIMENSIONS IN
MATERIAL THICKNESS	TOLERANCE (UNLESS OTHERWISE STATED)
DRAWN SG	DATE 27-3-97
CHECKED	DATE
APPROVED	DATE
MODEL TSL100/TSL122	



TITLE	
CRUNCH CHANNEL	
DWG.No	ISS
TL10-66-01.DGM	2

DWG.No. TL10-61-02.DGM



Global SPL bleed: ~10p via a switch. Preferred over pre-volume bleed for optimal tonal integrity.

Leave this stage at Unity for both outboard gear and IC3!

Reverb opamp (Lead IC2), a two-channel summing mixer with a switch, is not wired to provide summing but a simple pass-through, nevertheless it provides for "trailing" or "decay bleed-over" from either channel due to the way it is implemented. "Main TR3" works counter to this feature, coincidentally, but we removed it so no worries.

Option: R33 may be adjusted to help bring Dry signal up to unity gain, ganged along with the Wet, so that when the FX Loop is engaged (especially when the FX MIX is past "1") the signal maintains a nominal level. "Rear R39/41" can remain at stock value for this option.

Remove for faster switching between CL/OD, but will produce audible "pop". Option available.

Increases current to photocouplers

Remove treble bleed cap

Deep Boost: 16dB > 8dB

IC4: Try TL072ACP, or some other IC for better slew rate.

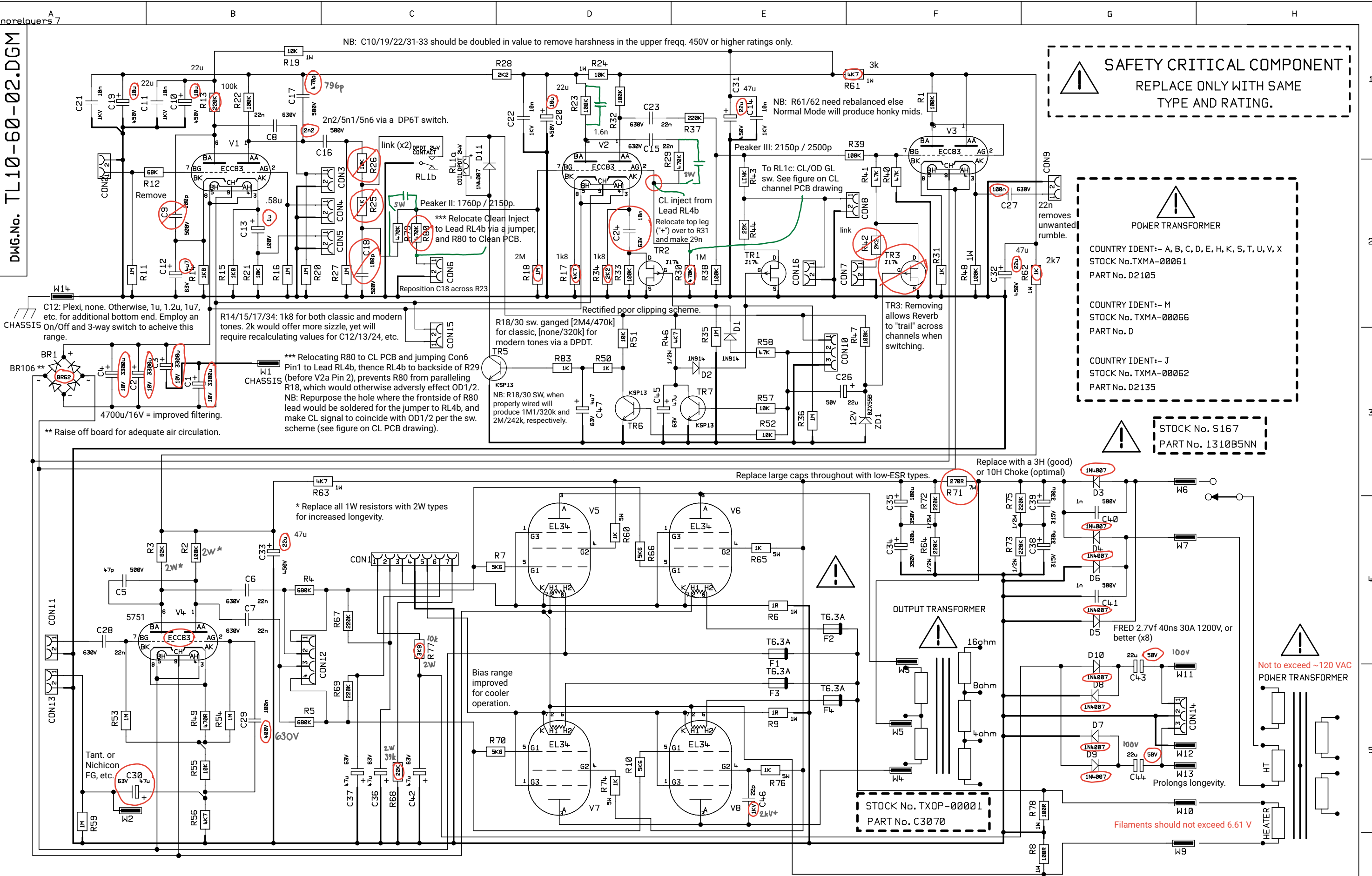
The Vesperado mod.

4	1622	21/4/98	ALL OTHER HOLES =	X = (THROUGH PLATED)
3	PRE-PROD MODS (1567)	23/3/98	A	E
2	PRE-PRODUCTION	2/12/97	B	F
1	PRE-PRODUCTION	20/10/97	C	G
ISS	ECO NUMBER	DATE	D	H

MATERIAL	DIMENSIONS IN
MATERIAL THICKNESS	TOLERANCE (UNLESS OTHERWISE STATED)
DRAWN SG	DATE 11-11_96
CHECKED	DATE
APPROVED	DATE
MODEL TSL100/TSL122	



TITLE	TSL 100 COMBO OVERDRIVE CHANNEL FRONT CONTROL PCB
DWG.No	TL10-61-02.DGM
ISS	4



20	2927	19-06-07
7	2395	16-07-03
6	2337	03-04-03
5	2207	3-10-02
4	2018	2-8-01
ISS	ECD NUMBER	DATE

ALL OTHER HOLES =		X = (THROUGH PLATED)	
A	E	J	N
B	F	K	P
C	G	L	Q
D	H	M	R

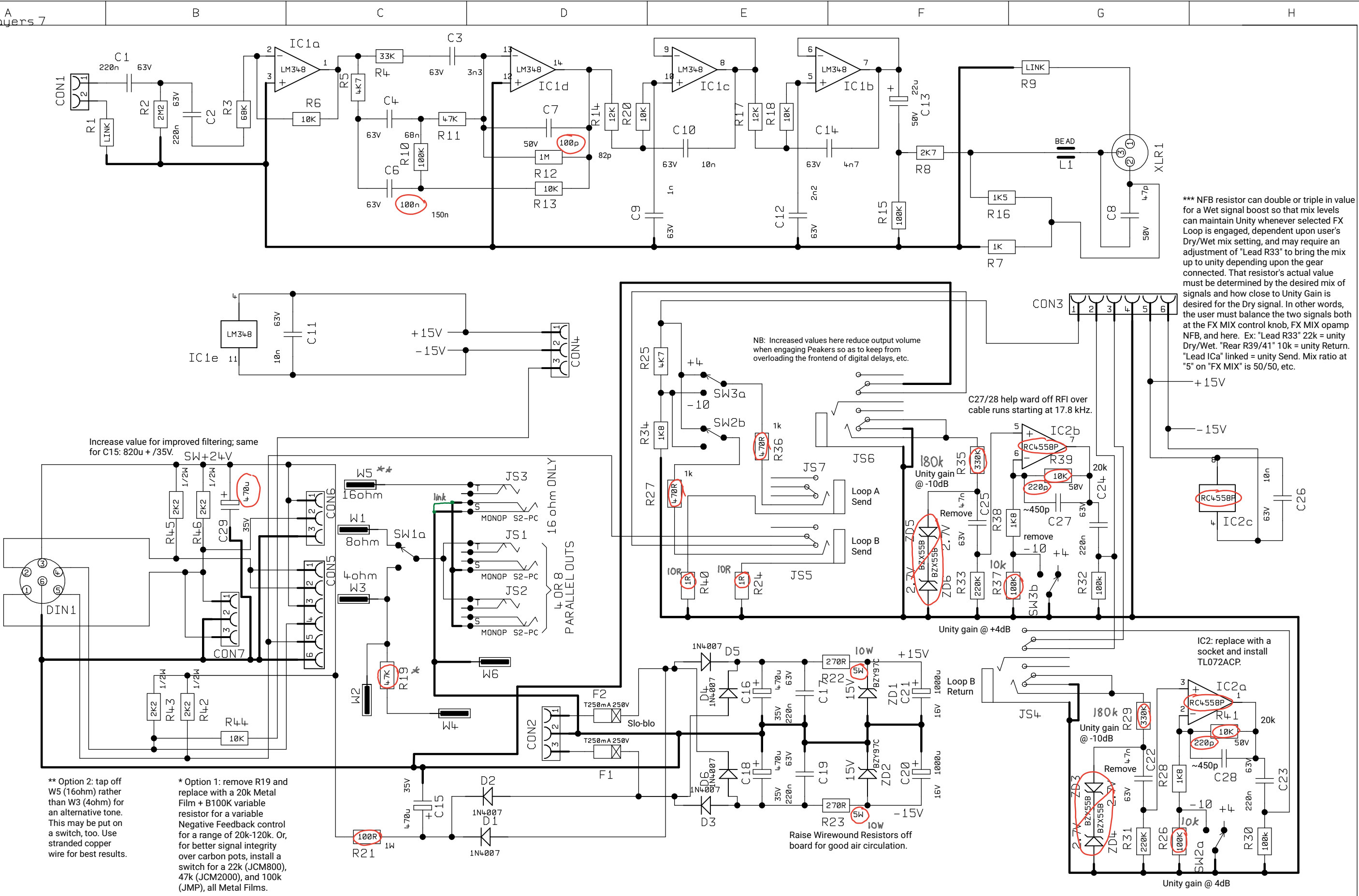
MATERIAL		DIMENSIONS IN	
MATERIAL THICKNESS		TOLERANCE (UNLESS OTHERWISE STATED)	
DRAWN S.G.	DATE 19/3/97	MODEL	
CHECKED	DATE	TSL100/TSL122	
APPROVED	DATE		

The Vesperado mod.



TITLE	TSL100/TSL122 CIRCUIT DIAGRAM
DWG.No	TL10-60-02.DGM
ISS	20

DWG.No. TL10-62-02.DGM



*** NFB resistor can double or triple in value for a Wet signal boost so that mix levels can maintain Unity whenever selected FX Loop is engaged, dependent upon user's Dry/Wet mix setting, and may require an adjustment of "Lead R33" to bring the mix up to unity depending upon the gear connected. That resistor's actual value must be determined by the desired mix of signals and how close to Unity Gain is desired for the Dry signal. In other words, the user must balance the two signals both at the FX MIX control knob, FX MIX opamp NFB, and here. Ex: "Lead R33" 22k = unity Dry/Wet. "Rear R39/41" 10k = unity Return. "Lead ICa" linked = unity Send. Mix ratio at "5" on "FX MIX" is 50/50, etc.

Increase value for improved filtering; same for C15: 820u + /35V.

** Option 2: tap off W5 (16ohm) rather than W3 (4ohm) for an alternative tone. This may be put on a switch, too. Use stranded copper wire for best results.

* Option 1: remove R19 and replace with a 20k Metal Film + B100K variable resistor for a variable Negative Feedback control for a range of 20k-120k. Or, for better signal integrity over carbon pots, install a switch for a 22k (JCM800), 47k (JCM2000), and 100k (JMP), all Metal Films.

NB: Increased values here reduce output volume when engaging Peakers so as to keep from overloading the frontend of digital delays, etc.

C27/28 help ward off RFI over cable runs starting at 17.8 kHz.

Unity gain @ +4dB

IC2: replace with a socket and install TL072ACP.

Raise Wirewound Resistors off board for good air circulation.

The Vesperado mod.

6	1663	10/9/98	ALL OTHER HOLES =	X = (THROUGH PLATED)
5	1631	24/4/98	A	E
4	1622	21/4/98	B	F
3	PRE-PROD MODS (1567)	23/3/98	C	G
ISS	ECO NUMBER	DATE	D	H

MATERIAL	DIMENSIONS IN
MATERIAL THICKNESS	TOLERANCE (UNLESS OTHERWISE STATED)
DRAWN SG	DATE 28-12-96
CHECKED	DATE
APPROVED	DATE



TITLE	JCM2000 REAR/JACK PCB
DWG.No	TL10-62-02.DGM
ISS	6